

Fig 1

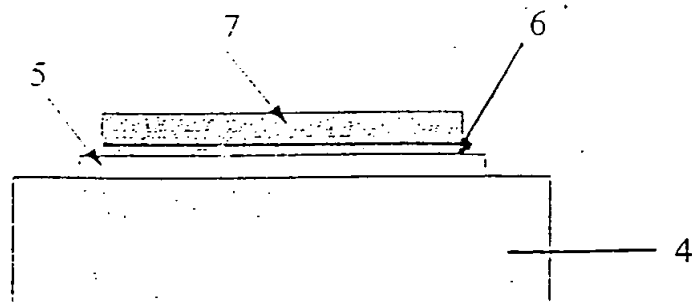


Fig 2

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A cross-sectional view of a semiconductor device. The substrate 8 has a stepped profile with five distinct levels. On the first level, there is a layer 10. On the second level, there is a layer 10. On the third level, there is a layer 11. On the fourth level, there is a layer 9. On the fifth level, there is a layer 9. A layer 12 is located on the top surface of the fifth level. A layer 13 is located on the top surface of the fourth level. A layer 14 is located on the top surface of the fifth level. A layer 15 is located on the top surface of the fifth level.

Fig 4

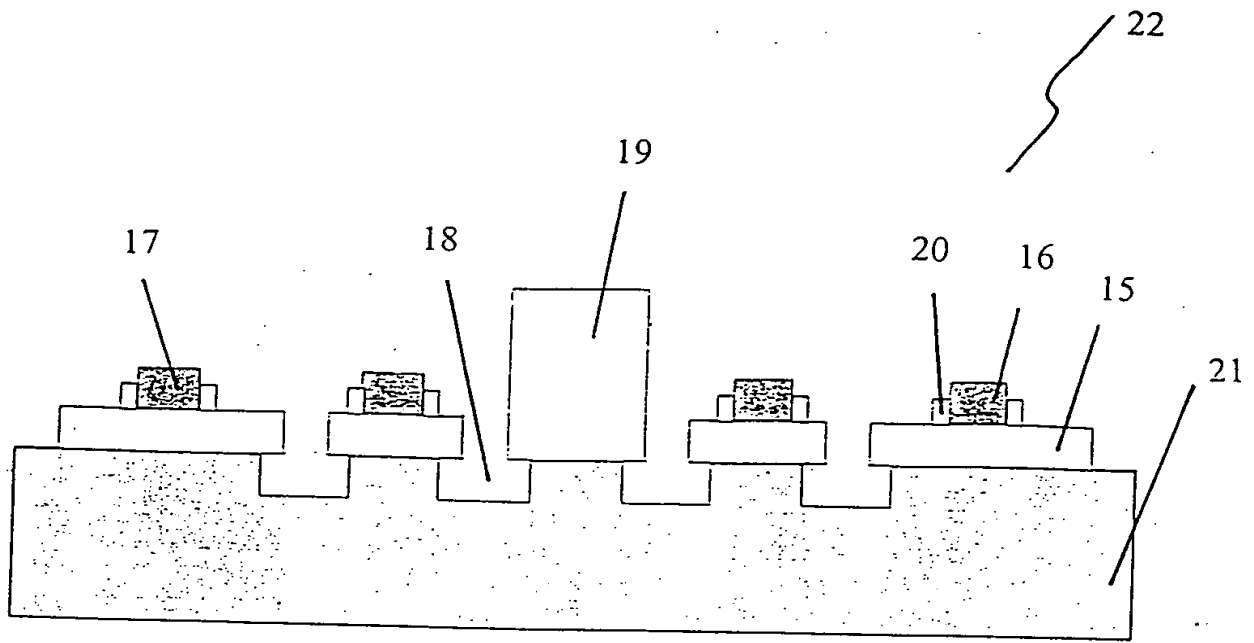


Fig 5

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